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REMARKS

The present response is intended to be fully responsive to all points of objection and/or rejection raised by the Office Action and is believed to place the application in condition for allowance. Favorable reconsideration and allowance of the application is respectfully requested.

Applicant asserts that the present invention is new, non-obvious and useful. Prompt consideration and allowance of the claims is respectfully requested.

Status of Claims

Claims 1-23 are pending in the application. Claims 18 and 19 have been objected to. Claims 1-23 have been rejected. Claims 1, 2, 4, 6, 7, 8, 13-19, 21 and 23 have been amended.

Claims 5, 9 and 20 have been canceled without prejudice or disclaimer. In making this cancellation without prejudice, Applicant reserve all rights in these claims to file divisional and/or continuation patent applications.

Remarks to the Specification

The amendments to the specification are editorial in nature and do not introduce new matter.

Claim Objections

The Office Action objected claims 18 and 19 because of alleged informalities. Claim 18 has been amended in order to cure these informalities. Accordingly, Applicant request withdrawal of the objection.

CLAIM REJECTIONS

35 U.S.C. § 112 Rejections

The Office Action rejected claims 1 and 18 under 35 U.S.C. § 112, second paragraph, as failing to comply with the written description requirement.

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Claims 1 and 18 have been amended to overcome the written description requirement rejection. It is respectfully asserted that the foregoing amendment merely addresses matters of form and does not change the literal scope of the claim in any way, and was not intended as an amendment narrowing the effective scope of such claim.

Applicant respectfully asserts that these amendments render claims 1 and 18 proper under 35 U.S.C § 112 and requests that the rejections be withdrawn.

The Office Action rejected claims 2 and 4 under 35 U.S.C. § 112, second paragraph, as having insufficient antecedent basis and claim 3 as depending on the rejected claim 2.

Claims 2 and 4 have been amended to overcome the antecedent basis deficiencies noted by the Action. It is respectfully asserted that the foregoing amendment merely addresses matters of form and does not change the literal scope of the claim in any way, and was not intended as an amendment narrowing the effective scope of such claim.

Applicant respectfully asserts that these amendments render claims 2-4 proper under 35 U.S.C § 112 and requests that the rejections be withdrawn.

35 U.S.C. § 102 Rejections

In the Action, claims 1-7 and 10-13 were rejected under 35 U.S.C. § 102(b), as being anticipated by a patent issued to Rossiter (US 4,573,136). Applicant believes this rejection has been overcome in view of the amendments made above and the remarks that follow.

Specifically, Applicant respectfully asserts that Rossiter does not teach or suggest the limitations of claim 1 as amended. Applicant respectfully submits that Rossiter does not teach an $\pi/4$ -shift QPSK modulator and at least the claimed feature of "...providing a portion of an address...in a reversed order from a Postcursor section of said register to a second memory portion at odd times...", as recited in amended claim 1.

Accordingly, Applicant respectfully asserts that a *prima facie* case over amended claim 1 cannot be established, and amended independent claim 1 is allowable. Accordingly, Applicant respectfully requests that the Examiner withdraw the rejections to amended independent claim 1.

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submits that claims 2-7 and 10-13 are patentable over the cited reference(s) by virtue of at least such dependency on patentable base claim 1. Accordingly, Applicant respectfully requests that the §102 rejection of claims 2-7 and 10-13 be withdrawn.

In the Action, claim 1 was also rejected under 35 U.S.C. § 102(b), as being anticipated by Testu Sakata et al., "A New Fully-Digitalized $\pi/4$ -Shift QPSK Modulator for Personal Communication Terminal", IEEE, ICUPC 1994, Pages 921-926. Applicant respectfully asserts that Testu Sakata et al. does not teach or suggest the limitations of claim 1 as amended.

Specifically, Testu Sakata et al. does not teach at least the claimed feature of "...providing a portion of an address...in a reversed order from a Postcursor section of said register to a second memory portion at odd times...", as recited in amended claim 1.

Accordingly, Applicant respectfully asserts that a *prima facie* case over amended claim 1 cannot be established, and amended independent claim 1 is allowable. Accordingly, Applicant respectfully requests that the Examiner withdraw the rejections to amended independent claim 1.

35 U.S.C. § 103 Rejections

In the Action, claims 8 and 9 were rejected under 35 U.S.C. § 103(a) as being unpatentable over Rossiter (US 4,573,136) in view of Testu Sakata et al., "A New Fully-Digitalized $\pi/4$ -Shift QPSK Modulator for Personal Communication Terminal", IEEE, ICUPC 1994, Pages 921-926. In response, Applicant respectfully traverses such rejections.

Claim 9 has been canceled without prejudice or disclaimer thus, the rejection of claim 9 is now moot. Applicant respectfully traverses the rejection of claim 8 as being unpatentable over Rossiter in view of Testu Sakata et al.. Applicant notes that Testu Sakata et al. is not cited as curing the deficiencies of Rossiter noted above and does not, in fact, cure such limitations. In this regard, without conceding the appropriateness of the combination, or the characterization of such references vis a vis claim 8, Applicant respectfully submits that the combination of the Rossiter in view of Testu Sakata et al. references fails to disclose or suggest that which is claimed in amended claim 1. Accordingly, in addition to any

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independent bases for patentability, Applicant respectfully submits that claim 8 is similarly patentable over the cited references by virtue of at least such dependency.

Accordingly, Applicant respectfully requests that the rejection of claim 8 under 35 U.S.C. § 103(a) be withdrawn.

The Office Action rejected claims 14-23 under 35 U.S.C. § 103(a), as being unpatentable over Rossiter (U.S. Patent 4,573,136 of record) in view of Misaizu (U.S. Patent 5,487,089 of record)

Applicant believes this rejection has been overcome in view of the amendments made above and the remarks that follow.

Regarding amended independent claim 14, the independent claim includes limitations that are not disclosed or suggested by the combination of Rossiter and Misaizu.

It is well established that obviousness requires a teaching or a suggestion by the prior art of all the elements of a claim (M.P.E.P. §2142). Without conceding the appropriateness of the combination, Applicant respectfully submits that the combination of Rossiter and Misaizu does not meet the requirements of an obvious rejection in that neither of the references disclose or suggests the element of "a register having a Precursor section to provide a first portion of an address in a direct order to a first memory portion and a Postcursor section to provide a second portion of the address in a reverse order to a second memory portion", as claimed in amended claim 14.

As discussed above, Rossiter disclosed a RAM computation and loading circuits 24 and 25 to load coefficients to RAM0 and RAM1 and addressing logic to provide the address of each loaded coefficient that attached to each RAM. Rossiter further disclosed an enabling gates 26 and 27 to enable the loading of the coefficients from loading circuits 24 and 25 for each RAM separately. The Office Action concedes that Rossiter does not involve a single register that provides most significant bits to both RAMs and a counter to provide least significant bits. Consequently, Rossiter does not contain any teaching or suggestion of which element that may alternate the portions of address to RAM0 and RAM1. Furthermore, Rossiter does not contain any teaching or suggestion of addressing RAM0 and RAM1 in odd and even times. Therefore, Rossiter cannot contain any teaching or suggestion of how to

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alternate the first and second portions of the address provided by first and second sections of the register to RAM0 and RAM1 according to even and odd times.

In addition, Applicant respectfully asserts that Misaizu is also devoid of any teaching or suggestion of a register having two portions wherein the first portion may address portions of memories in direct order and the second portion may address memories in reverse order. Furthermore, the Nyquist filter of Misaizu does not contain any teaching or suggestion of how to solve the problem of addressing memories in odd and even times. Therefore, Misaizu cannot be combined with Rossiter because each reference solves a different problem (*In re Fine*, 837 F.2d 347, 21 USPQ2d 1941 (Fed Cir. 1992)).

Since Rossiter and Misaizu, taken separately, are devoid of any teaching or suggestion of the limitations recited in amended claim 14, the combination of Rossiter and Misaizu must necessarily be devoid of the required teaching or suggestion of all the elements recited in amended claim 14. Consequently, the combination cannot make Applicant's amended claim 14 obvious.

Applicant would like to emphasize that the preceding paragraphs were not intended to attack Rossiter and Misaizu separately. But instead, Applicant has shown how each is devoid of claimed elements so that, by default, the combination is also devoid of at least some of the features of Applicant's claimed invention.

Applicant notes that claims 13-23 depend from claim 14. Thus, in addition to any independent bases for patentability, Applicant respectfully submits that claims 13-23 are similarly patentable over the cited references by virtue of at least such dependency. Accordingly, Applicant respectfully requests that the rejection of such claims be withdrawn.

In addition, Applicant note that Misaizu is not cited as curing the deficiencies of Rossiter and does not, in fact, cure such limitations. In this regard, without conceding the appropriateness of the combination, or the characterization of such references vis a vis claims 14-23, Applicant respectfully submits that the combination of the Rossiter and Misaizu references fail to disclose or suggest that which is claimed in amended claim 14. Accordingly, in addition to any independent bases for patentability, respectfully submit that claims 14-23 are similarly patentable over the cited references by virtue of at least such dependency.

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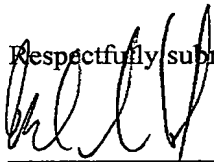
claims 14-23 are similarly patentable over the cited references by virtue of at least such dependency.

In view of the foregoing amendments and remarks, the pending claims are deemed to be allowable. Their favorable reconsideration and allowance is respectfully requested.

Should the Examiner have any question or comment as to the form, content or entry of this Amendment, the Examiner is requested to contact the undersigned at the telephone number below. Similarly, if there are any further issues yet to be resolved to advance the prosecution of this application to issue, the Examiner is requested to telephone the undersigned counsel.

Please charge any fees associated with this paper to deposit account No. 05-0649.

Respectfully submitted,



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